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PATENT NUMBER

Class | ISSUE CL

1104 U.S. PTO
09/814244
03/21/01

JC4 905

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED 8/2 Q.A. CH	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/814244		~~327~~ 716	13	~~2816~~ 2816	Cox Li

APPLICANTS: David Roe

TITLE: Method and apparatus for providing multiple clock signals on a chip using a second PLL library circuit connected to a buffered reference clock output of a first PLL library circuit

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)	
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)

INTERNATIONAL CLASSIFICATION

☐ Continued on Issue Slip inside File Jacket

☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
☐ The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		_____ (Date)	NOTICE OF ALLOWANCE MAILED	
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	_____ (Primary Examiner)		_____ (Date)	ISSUE FEE	
				Amount Due	Date Paid
☐ The terminal ___ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		_____ (Date)	ISSUE BATCH NUMBER	

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